

FIG. 1

FIG. 2 is a schematic diagram of a network device 210 connected to a patch panel 220 via a cable(s) 230. The network device 210 is connected to the patch panel 220 via the cable(s) 230. The network device 210 is connected to the patch panel 220 via the cable(s) 230.

200

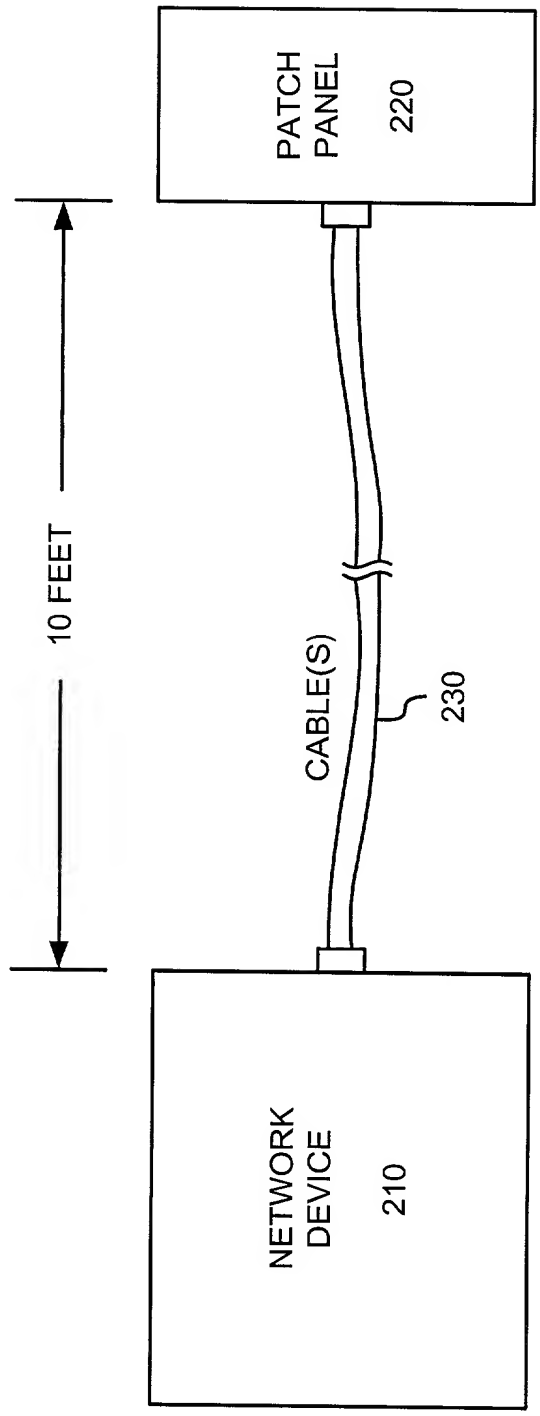


FIG. 2

210 ↗

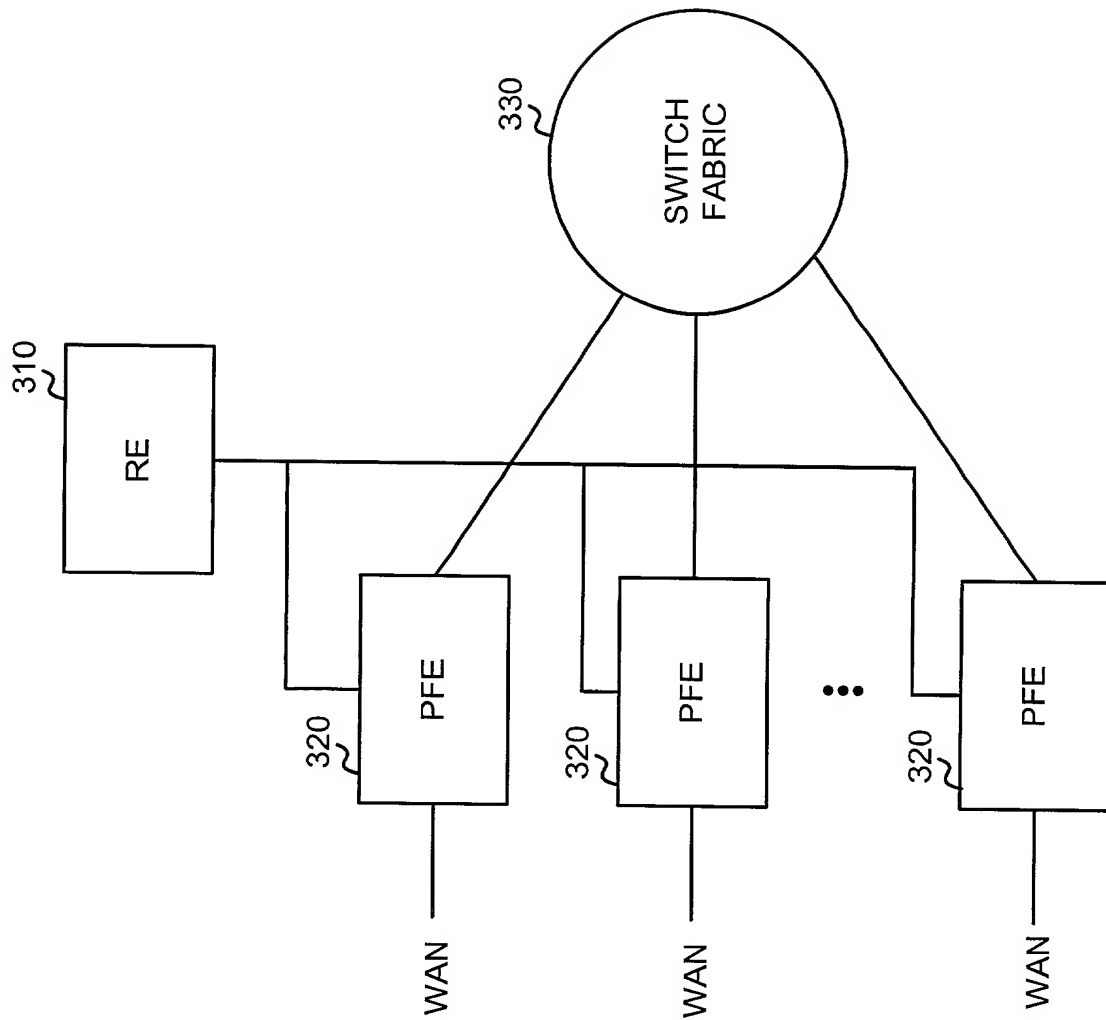


FIG. 3

FIG. 4 is a block diagram of a network device 320. The network device 320 includes a WAN interface 440, a PIC 440, a packet processor 410, a memory system 430, a packet processor 420, and a switch fabric 330. The network device 320 is connected to a RE 310 and a SWITCH FABRIC 330.

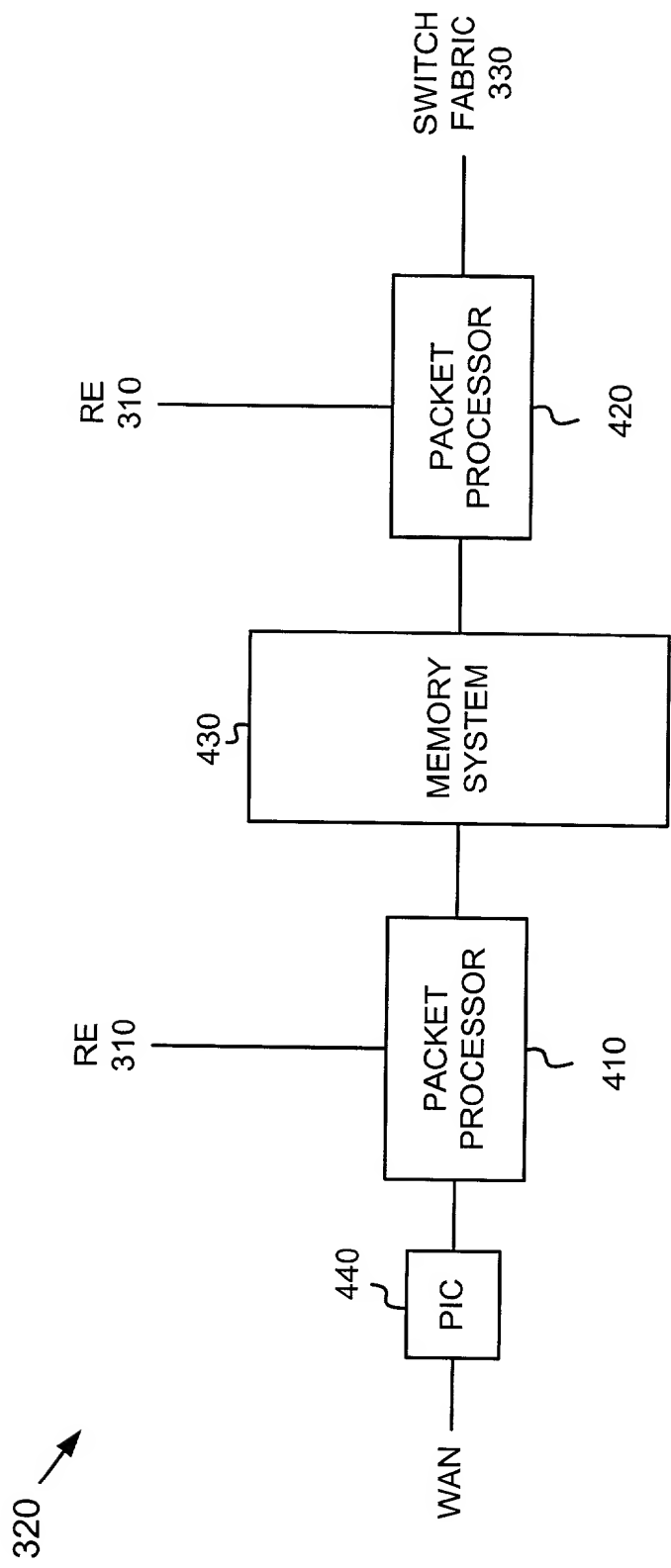


FIG. 4

440

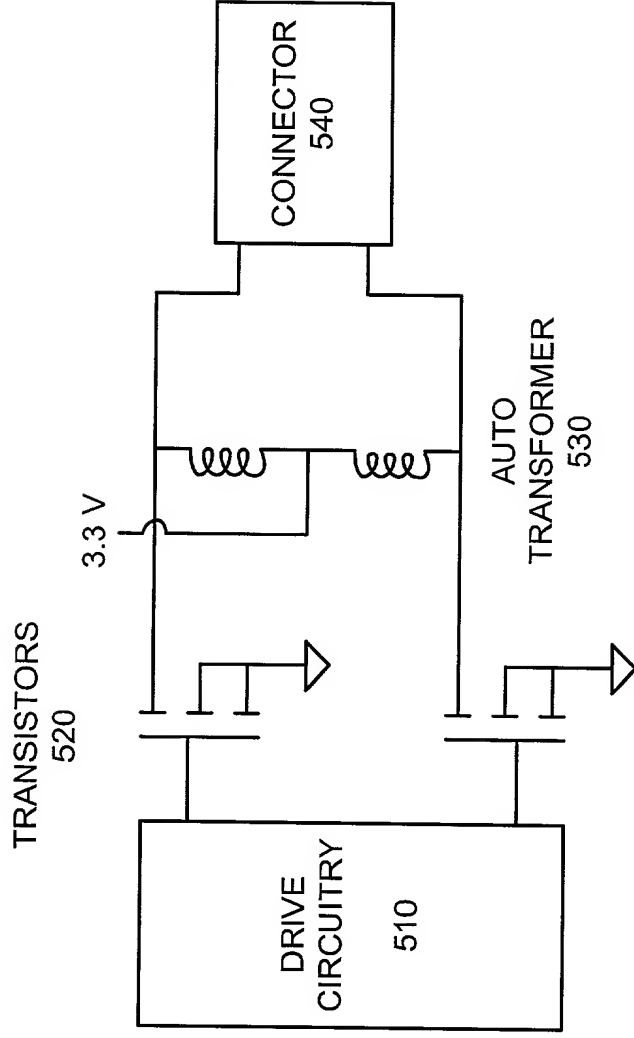


FIG. 5

230 →

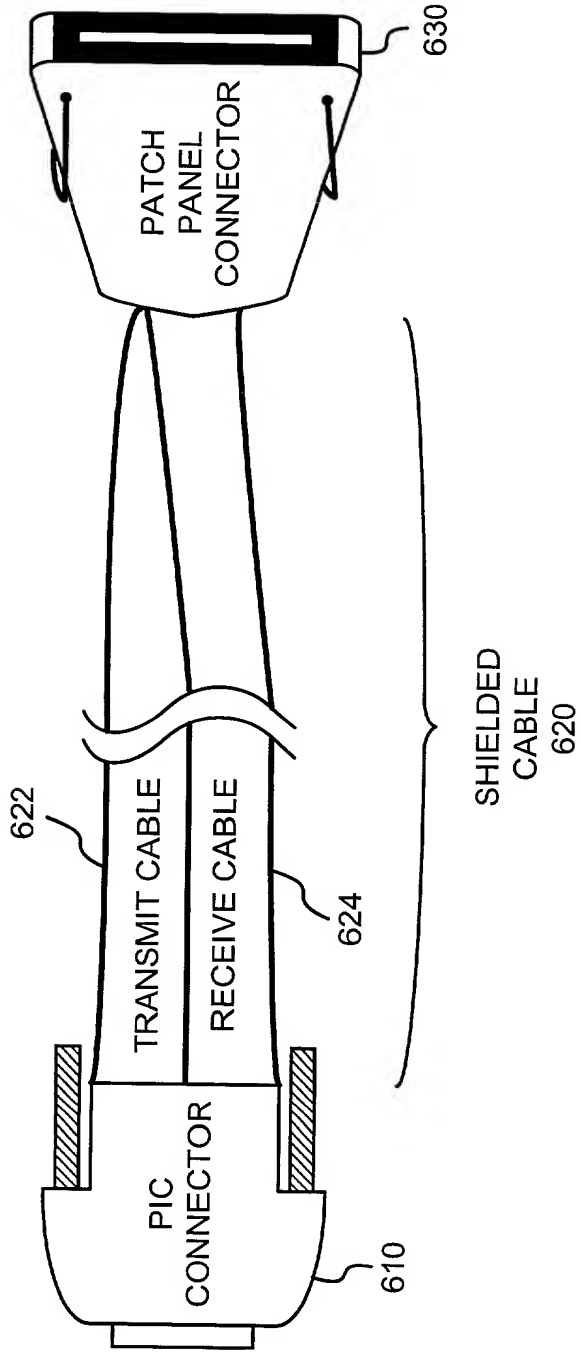
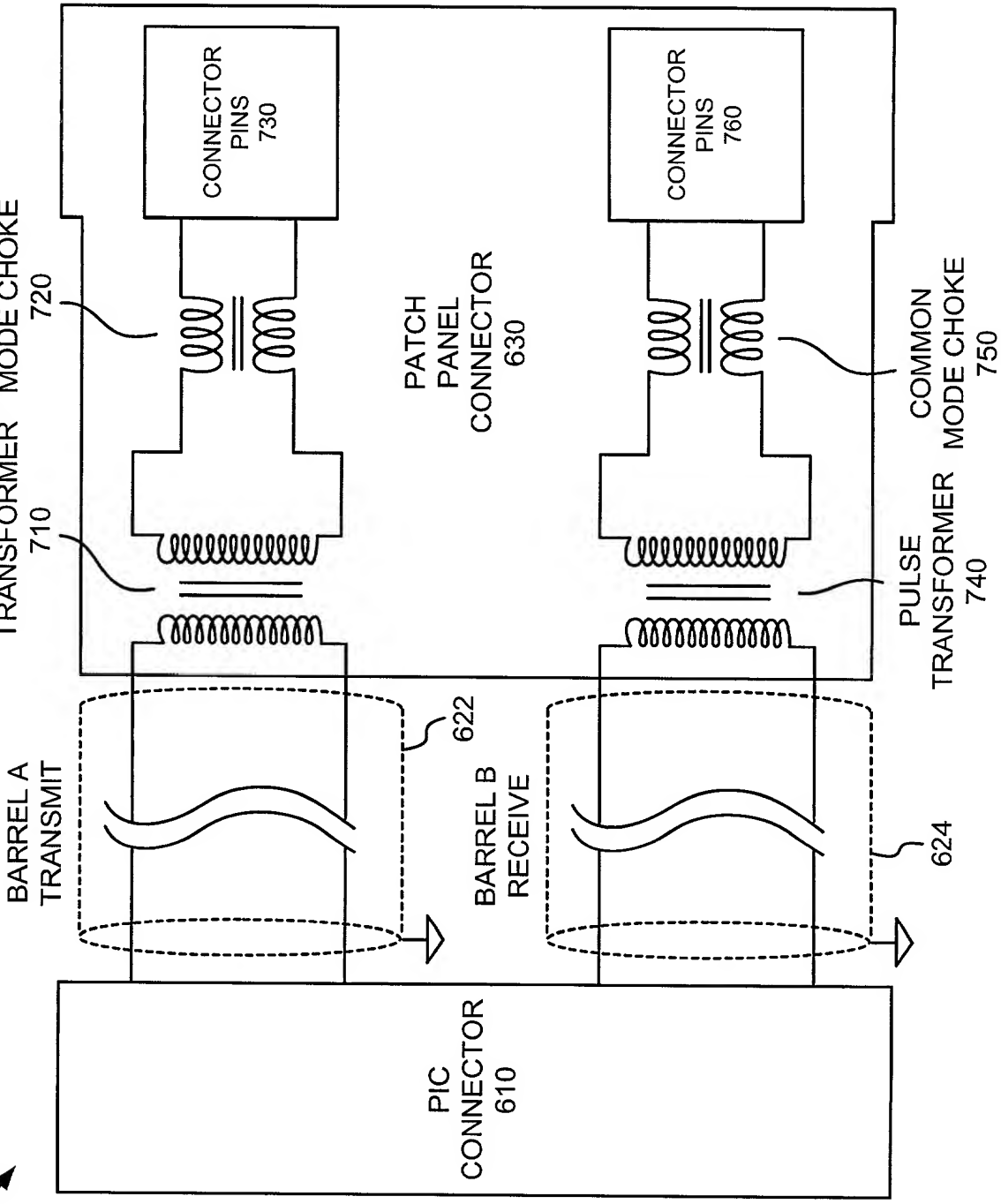


FIG. 6

FIG. 7

230



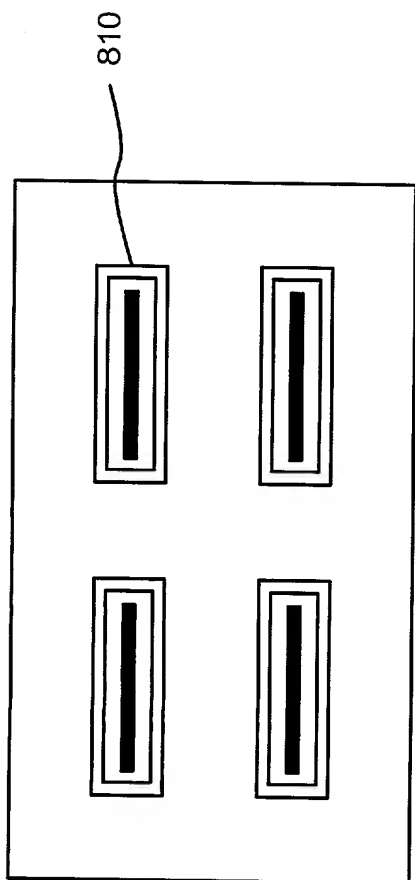


FIG. 8A

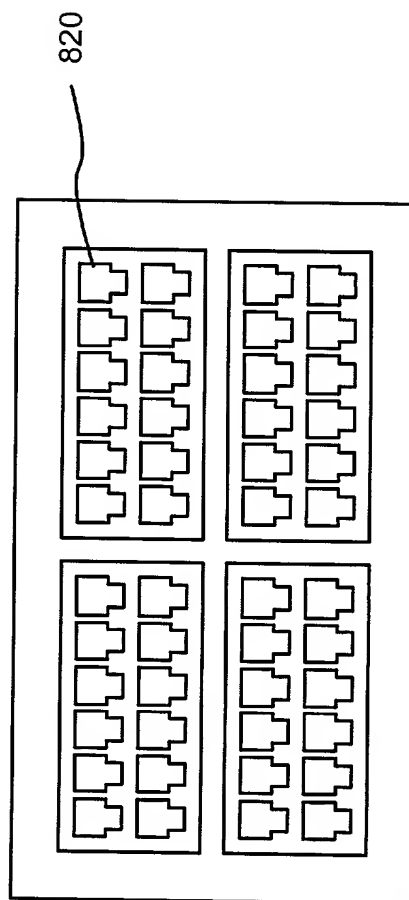


FIG. 8B